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## TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

		Application No.	10/683,542
		Filing Date	October 10, 2003
		First Named Inventor	Carl M. Ellison
		Art Unit	2186
		Examiner Name	Padmanabhan, Mano
Total Number of Pages in This Submission	9	Attorney Docket Number	42P9654C

### ENCLOSURES (check all that apply)

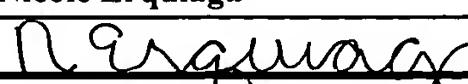
<input checked="" type="checkbox"/> Fee Transmittal Form <input checked="" type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment / Response <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input checked="" type="checkbox"/> PTO/SB/08 <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/ Incomplete Application <input type="checkbox"/> Basic Filing Fee <input type="checkbox"/> Declaration/POA <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s)	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): <div style="border: 1px solid black; height: 40px; width: 100%;"></div>
		Remarks

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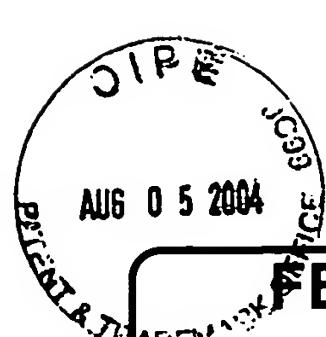
Firm or Individual name	Eric T. King, Reg. No. 44,188 BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
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# FEES TRANSMITTAL for FY 2004

Effective 01/01/2004. Patent fees are subject to annual revision.

Applicant claims small entity status. See 37 CFR 1.27.

**TOTAL AMOUNT OF PAYMENT** **(\$)**

<i>Complete if Known</i>	
Application Number	10/683,542
Filing Date	October 10, 2003
First Named Inventor	Carl M. Ellison
Examiner Name	Padmanabhan, Mano
Art Unit	2186
Attorney Docket No.	42P9654C

**METHOD OF PAYMENT** *(check all that apply)*

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Deposit  
Account  
Number 02-2666

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Account  
Name: Blakely, Sokoloff, Taylor & Zafman LLP

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## **FEE CALCULATION**

## 1. BASIC FILING FEE

Large Entity		Small Entity			Fee Paid
Fee Code	Fee (\$)	Fee Code	Fee (\$)	<u>Fee Description</u>	
1001	770	2001	385	Utility filing fee	
1002	340	2002	170	Design filing fee	
1003	530	2003	265	Plant filing fee	
1004	770	2004	385	Reissue filing fee	
1005	160	2005	80	Provisional filing fee	

## 2. EXTRA CLAIM FEES

		Extra Claims	Fee from below	Fee Paid
Total Claims	<input type="text"/>	$20^{**} =$ <input type="text"/>	$\times$ <input type="text"/>	$=$ <input type="text"/>
Independent Claims	<input type="text"/>	$3 =$ <input type="text"/>	$\times$ <input type="text"/>	$=$ <input type="text"/>
Multiple Dependent				$=$ <input type="text"/>

Large Entity		Small Entity		
Fee Code	Fee (\$)	Fee Code	Fee (\$)	<u>Fee Description</u>
1202	18	2202	9	Claims in excess of 20
1201	86	2201	43	Independent claims in excess of 3
1203	290	2203	145	Multiple Dependent claim, if not paid
1204	86	2204	43	**Reissue independent claims over original patent
1205	18	2205	9	**Reissue claims in excess of 20 and over

*\*\*or number previously paid, if greater. For Reissues, see below.*

**SUBMITTED BY**

**Complete (if applicable)**

SUBMITTED BY		Complete (if applicable)			
Name (Print/Type)	Eric T. King	Registration No. (Attorney/Agent)	44,188	Telephone	(714) 557-3800
Signature				Date	08/03/04

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TOTAL AMOUNT OF PAYMENT

(\$)

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Application Number	10/683,542
Filing Date	October 10, 2003
First Named Inventor	Carl M. Ellison
Examiner Name	Padmanabhan, Mano
Art Unit	2186
Attorney Docket No.	42P9654C

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 Deposit Account

Deposit Account Number **02-2666**

Deposit Account Name **Blakely, Sokoloff, Taylor & Zafman LLP**

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## FEE CALCULATION

## 1. BASIC FILING FEE

Fee Paid					
Large Entity	Small Entity	Fee Paid			
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1002	340	2002	170	Design filing fee	
1003	530	2003	265	Plant filing fee	
1004	770	2004	385	Reissue filing fee	
1005	160	2005	80	Provisional filing fee	
SUBTOTAL (1)			(\$)		

## 2. EXTRA CLAIM FEES

Total Claims	Independent Claims	Extra Claims	Fee from below	Fee Paid
		20**	=	
		3	=	
			=	

Large Entity	Small Entity	Fee Description	Fee Paid		
Fee Code	Fee (\$)	Fee Code	Fee (\$)		
1202	18	2202	9	Claims in excess of 20	
1201	86	2201	43	Independent claims in excess of 3	
1203	290	2203	145	Multiple Dependent claim, if not paid	
1204	86	2204	43	**Reissue independent claims over original patent	
1205	18	2205	9	**Reissue claims in excess of 20 and over original patent	
SUBTOTAL (2)			(\$)		

\*\*or number previously paid, if greater, For Reissues, see below

## 3. ADDITIONAL FEES

Large Entity	Small Entity	Fee Description	Fee Paid
Fee Code	Fee (\$)	Fee Code	Fee (\$)
1051	130	2051	65 Surcharge - late filing fee or oath
1052	50	2052	25 Surcharge - late provisional filing fee or cover sheet
2053	130	2053	130 Non-English specification
1812	2,520	1812	2,520 For filing a request for ex parte reexamination
1804	920*	1804	920* Requesting publication of SIR prior to Examiner action
1805	1,840*	1805	1,840* Requesting publication of SIR after Examiner action
1251	110	2251	55 Extension for reply within first month
1252	420	2252	210 Extension for reply within second month
1253	950	2253	475 Extension for reply within third month
1254	1,480	2254	740 Extension for reply within fourth month
1255	2,010	2255	1,005 Extension for reply within fifth month
1404	330	2401	165 Notice of Appeal
1402	330	2402	165 Filing a brief in support of an appeal
1403	290	2403	145 Request for oral hearing
1451	1,510	2451	1,510 Petition to institute a public use proceeding
1452	110	2452	55 Petition to revive - unavoidable
1453	1,330	2453	665 Petition to revive - unintentional
1501	1,330	2501	665 Utility issue fee (or reissue)
1502	480	2502	240 Design issue fee
1503	640	2503	320 Plant issue fee
1460	130	2460	130 Petitions to the Commissioner
1807	50	1807	50 Processing fee under 37 CFR 1.17(q)
1806	180	1806	180 Submission of Information Disclosure Stmt
8021	40	8021	40 Recording each patent assignment per property (times number of properties)
1809	770	1809	385 Filing a submission after final rejection (37 CFR § 1.129(a))
1810	770	2810	385 For each additional invention to be examined (37 CFR § 1.129(b))
1801	770	2801	385 Request for Continued Examination (RCE)
1802	900	1802	900 Request for expedited examination of a design application
Other fee (specify)			

\* Reduced by Basic Filing Fee Paid

SUBTOTAL (3)

(\$)

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Name (Print/Type)	Eric T. King	Registration No. (Attorney/Agent)	44,188	Telephone	(714) 557-3800	Complete (if applicable)
Signature						Date
					08/03/04	

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SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

CARL M. ELLISON, ET AL.

Application No.: 10/683,542

Filed: October 10, 2003

For: **CONTROLLING ACCESS TO  
MULTIPLE MEMORY ZONES IN AN  
ISOLATED EXECUTION  
ENVIRONMENT**

Art Group: 2186

Examiner: Padmanabhan, Mano

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). This IDS and IDS Citation Form are being submitted before the mailing of a first Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s). Some or all of the references listed on the enclosed PTO/SB/08 were previously identified in the parent application (Application No. 09/618,489, filed July 18, 2000) and copies of the references were furnished at that time. Accordingly, per 37 CFR §1.98(d)(1) additional copies of those references are not submitted herewith.

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

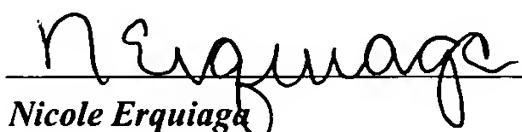


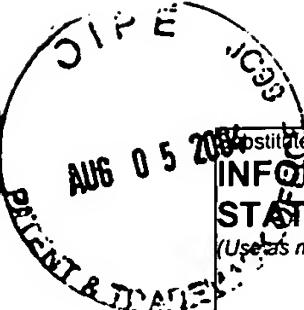
Eric T. King, Reg. No. 44,188

Date: August 03, 2004

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Date



Substitute for form 1449A/PTO  
**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
(Use as many sheets as necessary)

Sheet 1 of 4

<i>Complete if Known</i>	
<b>Application Number</b>	10/683,542
<b>Filing Date</b>	10/10/2003
<b>First Named Inventor</b>	Carl M. Ellison
<b>Group Art Unit</b>	2186
<b>Examiner Name</b>	Mano Padmanabhan

Attorney Docket No: 42390.P9654C

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T <sup>2</sup>
	DE-DE4217444	12/03/1992	Toyohisa, Imada , et al.			
	EP-EP0473913	03/11/1992	Farrell, Joel A.			
	EP-EP0600112	06/08/1994	Wimmer, Manfred D.			
	EP-EP0892521	01/20/1999	Zamek, Steven			
	EP-EP0930567	07/21/1999	Brewer, Jason M.			
	EP-EP0961193	12/01/1999	Laczko, Sr., Frank L.			
	EP-EP0965902	12/22/1999	Force, Gordon , et al.			
	EP-EP1030237	08/23/2000	Proudler, John , et al.			
	EP-EP1055989	11/29/2000	Proudler, Graeme J., et al.			
	EP-EP1056014	11/29/2000	Proudler, Graeme J., et al.			
	EP-EP1085396	03/21/2001	Proudler, Graeme J., et al.			
	EP-EP1146715	10/17/2001	Wang, Xin , et al.			
	EP-EP1271277	01/02/2003	Behar, Yaachov			
	JP-JP2000076139	03/14/2000	Tanno, Masaaki , et al.			
	WO-WO0021238	04/13/2000	Drews, Paul C.			
	WO-WO0062232	10/19/2000	Headrick, Samual P., et al.			
	WO-WO0127723	04/19/2001	Vamvakas, Athanasia , et al.			
	WO-WO0127821	04/19/2001	Chen, Liqun , et al.			
	WO-WO0163994	08/30/2001	Van Sant, Glen , et al.			
	WO-WO0175564	10/11/2002	Herbert, Howard C., et al.			
	WO-WO0175565	10/11/2001	Ellison, Carl M.			
	WO-WO0175595	10/11/2001	Ellison, Carl M., et al.			
	WO-WO0201794	01/03/2002	Ellison, Carl			
	WO-WO02060121	08/01/2002	Grawrock, David W.			
	WO-WO02086684	10/31/2002	Proudler, Graeme J.			
	WO-WO0217555	02/28/2002	Ford, Warwick			
	WO-WO03058412	07/17/2003	Glew, Andrew , et al.			
	WO-WO9524696	09/14/1995	Mooney, David M., et al.			
	WO-WO9729567	08/14/1997	Gressel, Carmi , et al.			
	WO-WO9812620	03/26/1998	Nishiuchi, Taiki , et al.			
	WO-WO9834365	08/06/1998	Devanbu, Premkumar T., et al.			
	WO-WO9844402	10/08/1998	Bramhill, Ian D., et al.			
	WO-WO9905600	02/04/1999	Garst, Blaine , et al.			
	WO-WO9909482	01/15/2002	Bausch, Jean			
	WO-WO9918511	04/15/1999	Edrich, David R.			

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Sheet 2 of 4		Attorney Docket No: 42390.P9654C				

### FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T <sup>2</sup>
	WO-WO9957863	11/11/1999	Hayes Jr., Kent F., et al.			
	WO-WO9965579	12/23/1999	Bond, Eugene T.			

### OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		BERG, CLIFF , "How Do I Create a Signed Applet?", <u>Dr. Dobb's Journal</u> , (08/1997),1-9	
		BRANDS, STEFAN , "Restrictive Blinding of Secret-Key Certificates", <u>SPRINGER-VERLAG XP002201306</u> , (1995),Chapter 3	
		CHIEN, ANDREW A., et al., "Safe and Protected Execution for the Morph/AMRM Reconfigurable Processor", <u>7th Annual IEEE Symposium, FCCM '99 Proceedings</u> , <u>XP010359180</u> , ISBN 0-7695-0375-6, Los Alamitos, CA, (4/21/1999),209-221	
		COMPAQ COMPUTER CORPORATION, "Trusted Computing Platform Alliance (TCPA) Main Specification Version 1.1a", <u>XP002272822</u> , (1/25/2001),1-321	
		COULOURIS, GEORGE , et al., "Distributed Systems, Concepts and Designs", <u>2nd Edition</u> , (1994),422-424	
		CRAWFORD, JOHN , "Architecture of the Intel 80386", <u>Proceedings of the IEEE International Conference on Computer Design: VLSI in Computers and Processors (ICCD '86)</u> , (October 6, 1986),155-160	
		DAVIDA, GEORGE I., et al., "Defending Systems Against Viruses through Cryptographic Authentication", <u>Proceedings of the Symposium on Security and Privacy</u> , IEEE Comp. Soc. Press, ISBN 0-8186-1939-2,(May 1989),	
		FABRY, R.S. , "Capability-Based Addressing", <u>Fabry, R.S., "Capability-Based Addressing," Communications of the ACM</u> , Vol. 17, No. 7, (July 1974),403-412	
		FRIEDER, GIDEON , "The Architecture And Operational Characteristics of the VMX Host Machine", <u>The Architecture And Operational Characteristics of the VMX Host Machine</u> , IEEE, (1982),9-16	
		GOLDBERG, ROBERT P., "Survey of Virtual Machine Research", <u>COMPUTER Magazine</u> , (06/1974),34-35	
		GONG, LI , et al., "Going Behind the Sandbox: An Overview of the New Security Architecture in the Java Development Kit 1.2", <u>Proceedings of the USENIX Symposium on Internet Technologies and Systems</u> , Monterey, CA,(12/1997),	
		GUM, P. H., "System/370 Extended Architecture: Facilities for Virtual Machines", <u>IBM J. Research Development</u> , Vol 27, Number 6, (11/1983),530-544	
		HEINRICH, JOE , "MIPS R4000 Microprocessor User's Manual, Second Edition", <u>Chapter 4 "Memory Management"</u> , (6/11/1993),61-97	
		HP MOBILE SECURITY OVERVIEW, "HP Mobile Security Overview", (Sept. 2002),1-10	

EXAMINER

DATE CONSIDERED

Substitute Disclosure Statement Form (PTO-1449)

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Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use as many sheets as necessary)		<i>Complete if Known</i> <b>Application Number</b> 10/683,542 <b>Filing Date</b> 10/10/2003 <b>First Named Inventor</b> Carl M. Ellison <b>Group Art Unit</b> 2186 <b>Examiner Name</b> Mano Padmanabhan	
Sheet 3 of 4		Attorney Docket No: 42390.P9654C	

**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		IBM, "Information Display Technique for a Terminate Stay Resident Program IBM Technical Disclosure Bulletin", <u>TDB-ACC-No. NA9112156</u> , Vol. 34, Issue 7A, (12/1/1991),156-158	
		IBM CORPORATION, "IBM ThinkPad T30 Notebooks", <u>IBM Product Specification</u> , located at <a href="http://www-1.ibm.com/services/files/cisco_t30_spec_sheet_070202.pdf">www-1.ibm.com/services/files/cisco_t30_spec_sheet_070202.pdf</a> , last visited June 23, 2004,(July 2, 2002),1-6	
		INTEL, "IA-32 Intel Architecture Software Developer's Manual", <u>Volume 3: System Programming Guide</u> , Intel Corporation - 2003,13-1 through 13-24	
		INTEL, "Intel386 DX Microprocessor 32-Bit CMOS Microprocessor With Integrated Memory Management", (1995),5-56	
		INTEL CORPORATION, "IA-64 System Abstraction Layer Specification", <u>Intel Product Specification</u> , Order Number 245359-001, (01/2000),1-112	
		INTEL CORPORATION, "Intel 82802AB/82802AC Firmware Hub (FWH)", <u>Intel Product Datasheet</u> , Document Number 290658-004,(November 2000),1-6, 17-28	
		INTEL CORPORATION, "Intel IA-64 Architecture Software Developer's Manual", <u>Volume 2: IA-64 System Architecture</u> , Order Number 245318-001, (01/2000),i, ii, 5.1-5.3, 11.1-11.8, 11.23-11.26	
		KARGER, PAUL A., et al., "A VMM Security Kernel for the VAX Architecture", <u>Proceedings of the Symposium on Research in Security and Privacy</u> , XP010020182, ISBN 0-8186-2060-9, Boxborough, MA, (5/7/1990),2-19	
		KASHIWAGI, KAZUHIKO , et al., "Design and Implementation of Dynamically Reconstructing System Software", <u>Software Engineering Conference</u> , Proceedings 1996 Asia-Pacific Seoul, South Korea 4-7 Dec. 1996, Los Alamitos, CA USA, IEEE Comput. Soc, US, ISBN 0-8186-7638-8,(1996),	
		LAWTON, KEVIN , et al., "Running Multiple Operating Systems Concurrently on an IA32 PC Using Virtualization Techniques", <a href="http://www.plex86.org/research/paper.txt">http://www.plex86.org/research/paper.txt</a> , (11/29/1999),1-31	
		LUKE, JAHN , et al., "Replacement Strategy for Aging Avionics Computers", <u>IEEE AES Systems Magazine</u> , XP002190614,(March 1999),	
		MENEZES, ALFRED J., et al., "Handbook of Applied Cryptography", <u>CRC Press LLC</u> , USA XP002201307, (1997),475	
		MENEZES, ALFRED J., et al., "Handbook of Applied Cryptography", <u>CRC Press Series on Discrete Mathematics and its Applications</u> , Boca Raton, FL, XP002165287, ISBN 0849385237,(Oct. 1996),403-405, 506-515, 570	
		MOTOROLA, "M68040 User's Manual", (1993),1-1 to 8-32	
		NANBA, S. , et al., "VM/4: ACOS-4 Virtual Machine Architecture", <u>VM/4: ACOS-4 Virtual Machine Architecture</u> , IEEE, (1985),171-178	
		RICHT, STEFAN , et al., "In-Circuit-Emulator Wird Echtzeittauglich", <u>Elektronic</u> , Franzis Verlag GMBH, Munchen, DE, Vol. 40, No. 16, XP000259620,(100-103),8-6-1991	

EXAMINER

DATE CONSIDERED

Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(Use as many sheets as necessary)</i>		<i>Complete if Known</i> <b>Application Number</b> 10/683,542 <b>Filing Date</b> 10/10/2003 <b>First Named Inventor</b> Carl M. Ellison <b>Group Art Unit</b> 2186 <b>Examiner Name</b> Mano Padmanabhan	
Sheet 4 of 4		Attorney Docket No: 42390.P9654C	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		ROBIN, JOHN S., et al., "Analysis of the Pentium's Ability to Support a Secure Virtual Machine Monitor", <u>Proceedings of the 9th USENIX Security Symposium</u> , XP002247347, Denver, Colorado, (8/14/00), 1-17	
		ROSENBLUM, M. , "Virtual Platform: A Virtual Machine Monitor for Commodity PC", <u>Proceedings of the 11th Hotchips Conference</u> , (8/17/1999), 185-196	
		RSA SECURITY, "Hardware Authenticators", <a href="http://www.rsasecurity.com/node.asp?id=1158">www.rsasecurity.com/node.asp?id=1158</a> , 1-2	
		RSA SECURITY, "RSA SecurID Authenticators", <a href="http://www.rsasecurity.com/products/securid/datasheets/SID_DS_0103.pdf">www.rsasecurity.com/products/securid/datasheets/SID_DS_0103.pdf</a> , 1-2	
		RSA SECURITY, "Software Authenticators", <a href="http://www.rsasecurity.com/node.asp?id=1313">www.rsasecurity.com/node.asp?id=1313</a> , 1-2	
		SAEZ, SERGIO , et al., "A Hardware Scheduler for Complex Real-Time Systems", <u>Proceedings of the IEEE International Symposium on Industrial Electronics</u> , XP002190615, (July 1999), 43-48	
		SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithm, and Source Code in C", <u>Wiley, John &amp; Sons, Inc.</u> , XP002939871; ISBN 0471117099, (Oct. 1995), 47-52	
		SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithm, and Source Code in C", <u>Wiley, John &amp; Sons, Inc.</u> , XP002138607; ISBN 0471117099, (Oct. 1995), 56-65	
		SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithms, and Source Code C", <u>Wiley, John &amp; Sons, Inc.</u> , XP0021111449; ISBN 0471117099, (Oct. 1995), 169-187	
		SCHNEIER, BRUCE , "Applied Cryptography: Protocols, Algorithms, and Source Code in C", 2nd Edition; <u>Wiley, John &amp; Sons, Inc.</u> , XP002251738; ISBN 0471128457, (Nov. 1995), 28-33; 176-177; 216-217; 461-473; 518-522	
		SHERWOOD, TIMOTHY , et al., "Patchable Instruction ROM Architecture", <u>Department of Computer Science and Engineering, University of California, San Diego, La Jolla, CA</u> , (Nov. 2001),	

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DATE CONSIDERED